



Module: PSDE4005 - Programmable Logic Controllers

Assessment: No 2 - PLC Program Design Using Ladder Logic

Student: Carys Williams

Assignment Marking Agreement Form (not to be seen by students)				
Mark Scheme	Marks Available	1st Marker	2nd Marker	← AGREED MARK AWARDED
1 - Research-informed Literature	10	6	✓	
2 - Knowledge and Understanding of Subject	25	22	✓	
3 - Analysis	25	20	✓	
4 - Practical Application and Deployment	30	28	✓	
5 - Skills for Professional Practice	10	7	✓	
Total	100	83		
Total Percentage	100%	83%		83%

1 st Marker Name	Richard Wray	1 st Marker Signature	<i>R Wray</i>
2 nd Marker Name	Jason Pointer	2 nd Marker Signature	<i>J Pointer</i>
Reasons for awarding the final mark where there is more than one marker			

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Student: Keegan Summers

Assignment Marking Agreement Form (not to be seen by students)				
Mark Scheme	Marks Available	1st Marker	2nd Marker	← AGREED MARK AWARDED
1 - Research-informed Literature	10	4	✓	
2 - Knowledge and Understanding of Subject	25	10	✓	
3 - Analysis	25	10	✓	
4 - Practical Application and Deployment	30	12	✓	
5 - Skills for Professional Practice	10	5	✓	
Total	100	41		
Total Percentage	100%	41%		41%

1 st Marker Name	Richard Wray	1 st Marker Signature	<i>R Wray</i>
2 nd Marker Name	Jason Pointer	2 nd Marker Signature	<i>SDP</i>

Reasons for awarding the final mark where there is more than one marker

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Student: Paul Miller

Assignment Marking Agreement Form (not to be seen by students)				
Mark Scheme	Marks Available	1st Marker	2nd Marker	← AGREED MARK AWARDED
1 - Research-informed Literature	10	5	✓	
2 - Knowledge and Understanding of Subject	25	15	✓	
3 - Analysis	25	15	✓	
4 - Practical Application and Deployment	30	25	✓	
5 - Skills for Professional Practice	10	7	✓	
Total	100	67		
Total Percentage	100%	67%		67%

1 st Marker Name	Richard Wray	1 st Marker Signature	<i>R Wray</i>
2 nd Marker Name	Jason Pointer	2 nd Marker Signature	<i>SDOLIT</i>

Reasons for awarding the final mark where there is more than one marker