



Complete, fold and seal
(see note 2 below)

SURNAME: Clark

FORENAME(S): Benjamin

Examination Answer Books

12 Pages

Instructions to Candidates

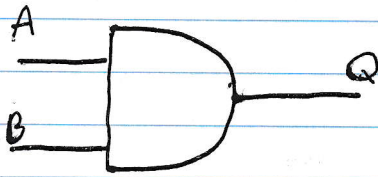
1. Please give the information requested in the boxes below.
2. Enter your name in the space provided to the right, fold over and seal down (using the label provided) to hide your name.
3. The answers must be legibly written. The Examiners can either ignore the script or have it typed and the expense of typing charged to the candidate.
4. Write your answers in ink.
5. Begin each answer on a fresh page. Do all rough work in the answer book and cross it through.
6. No part of this book is to be torn out; it must be handed in complete.
7. If more books than one are used, fasten them together using the tags available from the invigilator(s).
8. **Every book must be handed in. In no circumstances may answer books, used or unused, be taken out of the examination venue.**

1	Student Number	2309256
2	Title of Examination Paper	Micro Peripherals and interfaces
3	Module Code	ACDA4004
4	Date and Time of Examination	08/01/25 09:00
5	Examination Venue	310
6	Seat Number	025
7	Number of Answer Books handed in	

List the numbers of the questions you have attempted	Office Use
1	1
2	30
3	3
4	3
5	0
6	0
	7
Total	14%

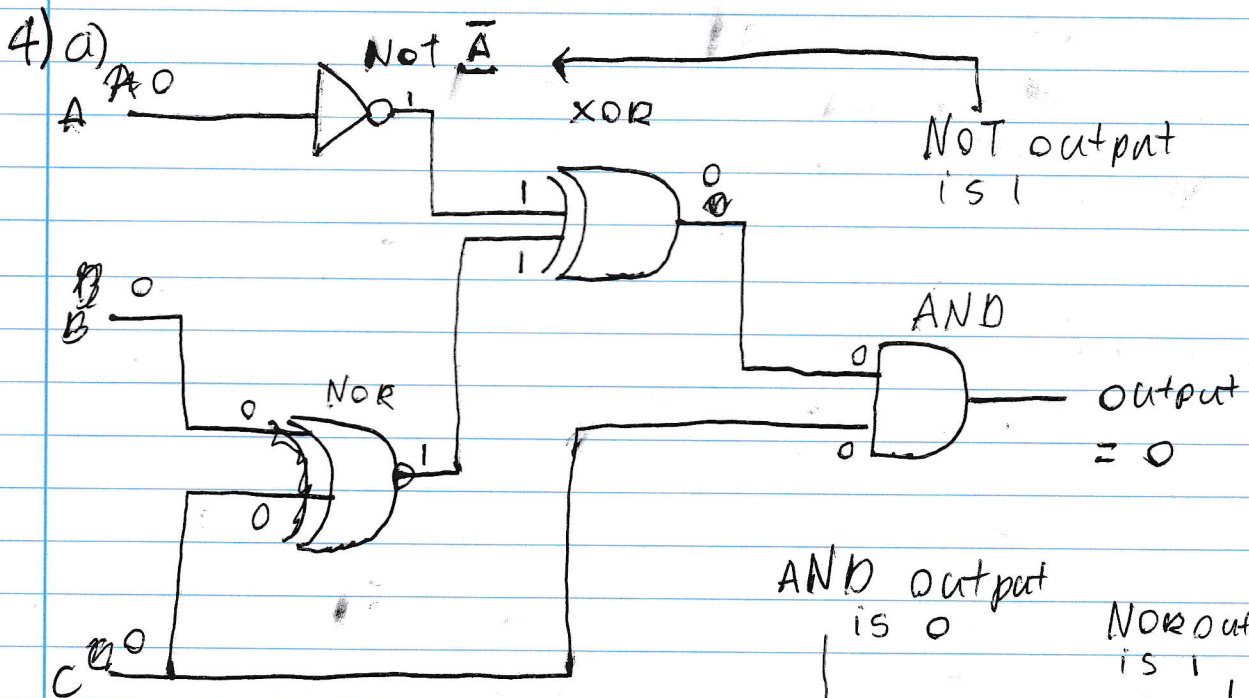
1
0
3
3
0
0
7

3) AND Gate



A	B	Q
0	0	0
0	1	0
1	0	0
1	1	1

A is the top input
B is the bottom input
Q is the output



XOR

A	B	Q
0	0	0
0	1	1
1	0	1
1	1	0

AND

A	B	Q
0	0	0
0	1	0
1	0	0
1	1	1

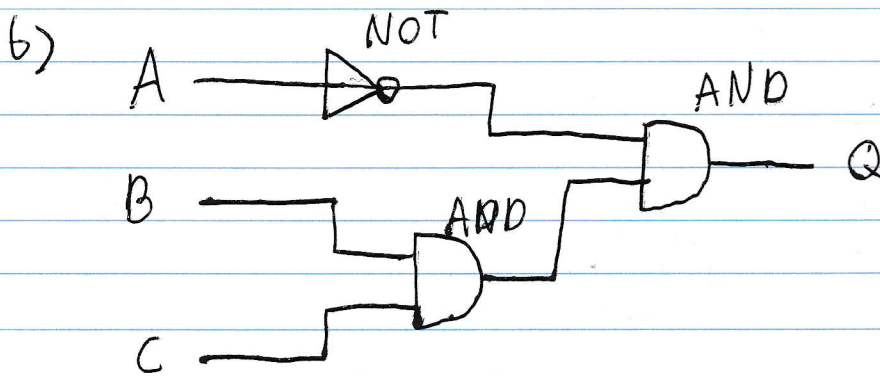
NOR

A	B	Q
0	0	1
0	1	0
1	0	0
1	1	0

XOR output is 0

60)

A	B	C	Q
0	0	0	0
0	0	1	0
0	1	0	0
1	0	0	0
1	0	1	0
1	1	0	0
0	1	1	1
1	1	1	0



1) a) RETURN

Return to previous - ^{Resets} ~~Returns~~ system to previous state

b) SLEEP

Sleep - instructs system to remain dormant

c) ADD LW

Add Left - Adds left function

d) BCF f,6

e) ~~DEC~~ F

Decreasing = Decrease

2)



Switch bounce refers to this, when a switch 'bounces' inbetween being switched on and off

5) Harvard

Von Neumann

Harvard architecture has the advantage of being 'simpler' ~~due to~~ than other forms of processor architecture.

Von Neumann has the advantage of being able to deal with larger tasks on a more complex scale.

6) ?