

UWTSD Timetable 2018/2019

Programme timetable - BENG-CSESH-L6-B3-SEP-FT-MP, BENG Computer Systems and Electronics (Wks 22-40, 21/01/2019 - 27/05/2019)

	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM	09:00PM
Mo													
Tuesday	05/02/2019 ... 28/05/2019 Group/Modules: PLCs & Control; PLCs & Control Staff: Gary Jones Room: SAIQ208				29/01/2019 ... 28/05/2019 Group/Module: Digital Systems Synthesis Staff: Gary Jones Room: SAIQ208								
We													
Thu			31/01/2019 ... 30/05/2019 Group/Module: Embedded Microcontrollers Staff: Gareth Jones Room: SAIQ208								31/01/2019 ... 30/05/2019 Group/Module: Major Project Staff: Gareth Jones; Gary Jones Room: SAIQ208		
Friday	25/01/2019 Group/Modules: Electronic System Design; Electronic System Design Staff: Gary Jones Room: SAIQ208						01/02/2019 ... 31/05/2019 Group/Modules: Major Project; Major Project Staff: Gareth Jones; Steve Mosey Room: SAIQ208						

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Friday	01/02/2019 ... 31/05/2019 Group/Module: Digital Systems Synthesis Staff: Gary Jones Room: SAIQ206												
Sat													
Sun													