

UWTSD Timetable 2017/2018

Programme timetable - BSC_CGDSH_L4 (1), BSC Computer Games Development L4 (1) (Wk 25, wk starting 12/02/2018)

	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM
Monday 12/02/2018	Computer Architecture and Networks Gareth Jones MH503 (SW); ZFACESA1_LS90 (LRA/MBL09/MH503/MT107/LRB)	Kaplan Radhakrishnan MH503 (SW); ZFACESA1_LS90 (LRA/MBL09/MH503/MT107/LRB)	Andrew Baker MH314 (SW); ZFACESA1_CL4 (MH314)				Michael Dacey; Tim Bashford MH305 (SW); ZFACESA1_CL5 (MH302/MH305/MH306)	
Tue 13/02/2018								
Wed 14/02/2018	Information Engineering Beverley Holland MH306 (SW); ZFACESA1_CL5 (MH302/MH305/MH306)		Gareth Jones MH303/4 (SW)	John Rees MH505 (SW); ZFACESA1_CL4 (MH314)		Michael Dacey; Tim Bashford MH305 (SW)		
Thu 15/02/2018	Michael Dacey; Tim Bashford MH306 (SW); ZFACESA1_CL5 (MH302/MH305/MH306)		Michael Dacey MH305 (SW); ZFACESA1_CL5 (MH302/MH305/MH306)			Maths for Software Development Gordon Dickers MH411 (SW); ZFACESA1_CL2 (MH411)		
Fri 16/02/2018								