

UWTSD Timetable 2017/2018



Programme timetable - BENG_CSESH_L5 (2), BENG Computer Systems & Electronics L5 (2) (Wk 23, wk starting 29/01/2018)

	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM
Mo 29/01/2018	Group Project MH507 (SW); ZFACESA1_Elec&Electronics Electrical & Electronics Lab (MH507/MH508)							
Tue 30/01/2018	Digital Electronics Steve Mosey MH506 (SW)		Research & Development Methods; Research Methods Nicola Whitehead MH314 (SW); ZFACESA1_CL4 (MH314)					
Wed 31/01/2018	Project Management Susan Maw MBG06 (SW); ZFACESA1_CL3 (MBG06)		Object Oriented Programming Jaymie Phillips MH305 (SW); ZFACESA1_CL5 (MH302/MH305/MH306)					
Thu 01/02/2018	Circuit Design & Analysis; Circuit Design & Analysis Steve Mosey MH507 (SW)		Network Operating Systems & Security Tim Bashford MH314 (SW); ZFACESA1_CL4 (MH314)					
Fri 02/02/2018								