

Computer Systems Exam

i)

1. AND Gate

2. OR Gate

3. Not Gate

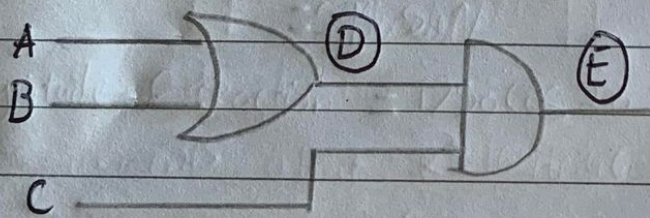
4. XOR Gate

5. NAND Gate

ii) AND Gate Truth Table

A	B	Output
0	0	0
0	1	0
1	0	0
1	1	1

iii) Indicate the Stages at logic level 1



$$D = 1$$

$$E = 1$$