

UWTSD Timetable 2017/2018

Programme timetable - BENG_CSESH_L4 (1), BENG Computer Systems & Electronics L4 (1) (Wk 7, wk starting 09/10/2017)

	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM
Monday			09/10/2017 Group/Module: Academic & Employability Skills Staff: Gareth Jones Rooms: MH503 (SW); ZFACESA1_LS90 (LRA/MBL09/MH503/MT107/LRB)			09/10/2017 Group/Module: Study & Workshop Skills Staff: Gary Jones; Steve Mosey Room: MH506 (SW)		09/10/2017 Staff: Gaynor Thomas Rooms: MH411 (SW); ZFACESA1_Games (MH312)			
Tuesday	10/10/2017 Staff: Gordon Dickers Room: MH411 (SW)		10/10/2017 Group/Module: Electrical & Electronic Principles Staff: Steve Mosey Rooms: MH507 (SW); ZFACESA1_Elec&Electronics Electrical & Electronics Lab (MH507/MH508)			10/10/2017 Staff: Gareth Jones Rooms: MH507 (SW); ZFACESA1_Elec&Electronics Electrical & Electronics Lab (MH507/MH508)					
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	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM
Thursday		12/10/2017 Group/ Module: Academic & Employability Skills Staff: Gareth Jones Rooms: MH305 (SW); ZFACESA1 CL5 (MH302/ MH305/ MH306)									
Fri											