

2114210 Assignment 1

Electronic Systems Design JL 1

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by Jack Lonsdale

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69/100

GRADING FORM: SMU GENERIC_GJ

JACK LONSDALE

69.25

RESEARCH-INFORMED LITERATURE



Good literature review of available systems with a fairly good comparison table of relevant information but would have been better with some images.60%

6

KNOWLEDGE AND UNDERSTANDING



Your schematic diagram is good but there are a couple of issues. There are no voltage regulators shown in the circuit diagram. Your PCB has been routed and is showing zero errors, although this cannot be checked as the

16.25

pcb files were not included. I would like to see the PCB and Proteus files. A good attempt 65%

ANALYSIS



Good selection of microcontroller, and of the enclosure. Reasonable selection of sensors including an image and short spec. A 100nf cap should be placed between the power lines as close to the micro-controller as possible. Your connectors are good. 70%

24.5

PRACTICAL APPLICATION AND DEP.



your costings are nicely tabulated and carry supplier, manufacturer and part numbers. Reasonable attempt at future enhancements. 75%

15

SKILLS FOR PROFESSIONAL PRAC.



The report is well presented with some good explanations of choices and images. Very good selection of references. 75%

7.5