



Module: PSDE4021 - Electrical Technology

Assessment: No 1 - DC Circuit Lab

Student: Sean Beynon

Assignment Marking Agreement Form (not to be seen by students)				
Mark Scheme	Marks Available	1st Marker	2nd Marker	← AGREED MARK AWARDED
1 - Research-informed Literature	10	6.9	✓	
2 - Knowledge and Understanding of Subject	20	17.1	✓	
3 - Analysis	20	13.6	✓	
4 - Practical Application and Deployment	40	31.6	✓	
5 - Skills for Professional Practice	10	6	✓	
Total	100	75.2		
Total Percentage	100%	75%		75%

1 st Marker Name	Steve Roberts	1 st Marker Signature	<i>S Roberts</i>
2 nd Marker Name	Jason Pointer	2 nd Marker Signature	<i>JDP</i>

Reasons for awarding the final mark where there is more than one marker



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Student: Richie Jones

Assignment Marking Agreement Form (not to be seen by students)				
Mark Scheme	Marks Available	1st Marker	2nd Marker	← AGREED MARK AWARDED
1 - Research-informed Literature	10	4.9	✓	
2 - Knowledge and Understanding of Subject	20	13.8	✓	
3 - Analysis	20	11.8	✓	
4 - Practical Application and Deployment	40	27.6	✓	
5 - Skills for Professional Practice	10	6.9	✓	
Total	100	65		
Total Percentage	100%	65%		65%

1 st Marker Name	Steve Roberts	1 st Marker Signature	<i>S Roberts</i>
2 nd Marker Name	Jason Pointer	2 nd Marker Signature	<i>J Pointer</i>

Reasons for awarding the final mark where there is more than one marker

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Student: Jac Williams

Assignment Marking Agreement Form (not to be seen by students)				
Mark Scheme	Marks Available	1st Marker	2nd Marker	← AGREED MARK AWARDED
1 - Research-informed Literature	10	4.5	✓	
2 - Knowledge and Understanding of Subject	20	8	✓	
3 - Analysis	20	0	✓	
4 - Practical Application and Deployment	40	16	✓	
5 - Skills for Professional Practice	10	4	✓	
Total	100	32.5		
Total Percentage	100%	33%		

1 st Marker Name	Steve Roberts	1 st Marker Signature	<i>S Roberts</i>
2 nd Marker Name	Jason Pointer	2 nd Marker Signature	<i>JDP</i>
Reasons for awarding the final mark where there is more than one marker			