



Complete, fold and seal
(see note 2 below)

Reggie W

12 Pages

Instructions to Candidates

1. Please give the information requested in the boxes below.
2. Enter your name in the space provided to the right, fold over and seal down (using the label provided) to hide your name.
3. The answers must be legibly written. The Examiners can either ignore illegible script or have it typed and the expense of typing charged to the candidate.
4. Write your answers in ink.
5. Begin each answer on a fresh page. Do all rough work in the answer book and cross it through.
6. No part of this book is to be torn out; it must be handed in complete.
7. If more books than one are used, fasten them together using the tags available from the invigilator(s).
8. **Every book must be handed in. In no circumstances may answer books, used or unused, be taken out of the examination venue.**

1	Student Number	key 1039618 nicko pet: phedull and inter face
2	Title of Examination Paper	~~Control Nickod~~ ~~electric electronic~~ brought en ~~en~~ ~~en~~
3	Module Code	6066 u 6066
4	Date and Time of Examination	17/05/2024 14:00pm Dino
5	Examination Venue	Wen Hall
6	Seat Number	D2
7	Number of Answer Books handed in	4

List the numbers of the questions you have attempted	Office Use
Q1	0
Q2	0
Q3	1
Q4	0
Q5	0
Total	1 out of 5

(Gweler yr ochr arall am y fersiwn Cymraeg)

RK

1 out of 50

2%

Q1

Q1
0/10

1.1

Control unit is the control for data around the CPU X

1.1.2 X

1.1.3. Memory hierarchy

is when data store onto the computer X

DMA X

1.2

Programming language is a code that to program circuit unit X

Machine language is for a program the machine ~~write~~ takes X they have to ~~write~~ the ,

1. Machine language is the machine to X

Q2

Q2
0/10

3 is a connection to cable to which controller X

4 CPU is ~~transfer of~~ it controller data X

1.1.4. DMA is

is ~~data transfer~~

Q3

3-?

a CPU will finish what they doing before
having to wait job X

①

61 ~~was~~ because it's done in order

C1 ~~knows~~ he has to wait till finish
they know they doing X

Q5

USB state for X

Q3

1/10

Q4

0/10

Q5

0/10